



Lr No: PACE(A)/ECS/2026-27/EE/10

18-08-2026

CIRCULAR

All the students are hereby informed that III B.Tech II Semester (R21) Supplementary (2021,2022 Admitted batches) Examinations will commence from **26-08-2026**. The Time Table for these examinations is given below.

Branch	Timing of the Semester End Examinations: 10:00 am to 1:00 pm				
	Date & Day				
	26-08-2026 (Wed Day)	27-08-2026 (Thur Day)	28-08-2026 (Fri Day)	29-08-2026 (Sat Day)	31-08-2026 (Mon Day)
CIVIL	Water Resources Engineering	Foundation Engineering	Introduction to Database Management Systems Environmental Impact Assessment	Employability Skills	Design and Drawing of Steel Structures
EEE	Switch gear and protection	Linear & Digital IC Applications	Utilisation Of Electrical Energy Power Semiconductor Drives	Design thinking for Innovation	Power system analysis
ME	Heat & Mass Transfer	Design of Machine Elements -II	Renewable Sources of Energy Production Planning Control	Constitution of India	Metrology & Measurements
ECE	Internet of Things	VLSI Design	Optical Fiber Communication Digital Image Processing	Intellectual Property Rights	Microwave & Radar Engineering
CSE	Advanced Java and Web Technologies	Automata Theory and Compiler Design	Introduction to Simulation Software	Entrepreneurship and Innovation	Cryptography and Network Security
CS&IT	Machine Learning	Automata Theory and Compiler Design	Management Science	Design thinking for Innovation	Cryptography and Network Security
IT	Machine Learning	Design and Analysis of Algorithms	Management Science	Design Thinking for innovation	OOAD Using UML
CSE (IOT&CSBT)	Reinforcement Learning	Design and Analysis of Algorithms	Embedded Systems Management Science	Design Thinking for innovation	Sensor Network Technologies
AI&DS	Deep Learning	Design & Analysis of Algorithms	Introduction to Simulation Software	Design Thinking For Innovation	Data Science
AI&ML	Deep Learning	Design & Analysis of Algorithms	Introduction to Simulation Software	Design Thinking For Innovation	Data Science
CSE (RL)	Advanced Java and Web Technologies	Automata Theory and Compiler Design	IPR&PATENTS	Entrepreneurship and Innovation	Cryptography and Network Security

NOTE:

- i) Any Omissions or clashes in this Time Table may please be informed to the Controller of Examinations immediately
- ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual


CONTROLLER OF EXAMINATIONS
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PACE INSTITUTE OF TECHNOLOGY & SCIENCES
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VALLUR, ONGOLE-523 272, ANDHRA PRADESH


PRINCIPAL

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